



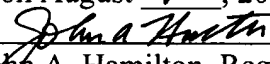
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.: 10/817,207 Confirmation No.: 2438
Applicant: Kenneth S. Goekjian *et al.*
Filed: April 2, 2004
For: A CONTEXT-BASED DIRECT MEMORY ACCESS ENGINE
FOR USE WITH MEMORY SYSTEM SHARED BY DEVICES
ASSOCIATED WITH MULTIPLE INPUT AND OUTPUT PORTS
Art Unit: 2438
Examiner: Ernest Unelus
Docket No.: A2004003
Customer No.: 26643

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

The undersigned hereby certifies that this document is being placed in the United States mail with first-class postage attached, to Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on August 8, 2006.


John A. Hamilton, Reg. No. 48,946

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

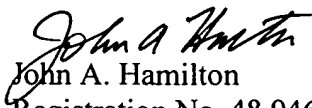
TRANSMITTAL LETTER

Transmitted herewith for filing in the above patent application is a Transmittal Of Information Disclosure Statement, Form PTO-SB/08A/B and copies of non U.S. patent references cited.

Please charge **Deposit Account No. 500876** in the amount of **\$180.00** fee. The Commissioner is hereby authorized to charge any additional fees which may be required or credit any overpayment to said Deposit Account.

Dated: August 8, 2006

Respectfully submitted,


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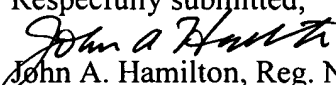
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**TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT
BEFORE MAILING DATE OF EITHER A FINAL ACTION
OR NOTICE OF ALLOWANCE (37 CFR § 1.97 (c))**

Transmitted herewith for filing in the above-entitled patent application is a PTO Form SB-08A/B and copies of non U.S. patent references cited. The information disclosure statement is being filed *before* the mailing of either a final action under § 1.113, or a notice of allowance under § 1.311, whichever occurs first. Applicant requests consideration of the document.

Applicant submits the fee set forth in § 1.17(p). Please charge **Deposit Account No. 50-0876** the amount of **\$180.00** fee. The Commissioner is hereby authorized to charge any additional fees which may be required or credit any overpayment to said Deposit Account.

Dated: August 8, 2006

Respectfully submitted,

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FORM PTO-SB/08-172 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	ATTY. DOCKET NO.: A2004003	SERIAL NO.: 10/817,207
	APPLICANT: Kenneth S. Goekjian et al.	
	FILING DATE: 04/02/2004	GROUP: 2828

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			Colnaric, Matjaz, et al, "Architectural Support For Predictability In Hard Real Time Systems", Control Engineering Practice, 1(1):51-59, Feb. 1993, ISSN 0967-0061, 8 pages.
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			Markatos, Evangelos P., et al, "User-Level DMA without Operating System Kernel Modification", In Proceeding of the Third Int'l Symposium on High-Performance Computer Architecture, (HPCA), San Antonio, TX, 1997, 11 pages.
			Srinivasan, Sujaya, et al, "An Interprocess Communication Mechanism to Enagle Real-Time Component-Based Software for Embedded Microcontrollers", Conference on Compiler and Architectural Support for Embedded Systems, Washington, DC, October 1999, 5 pages.